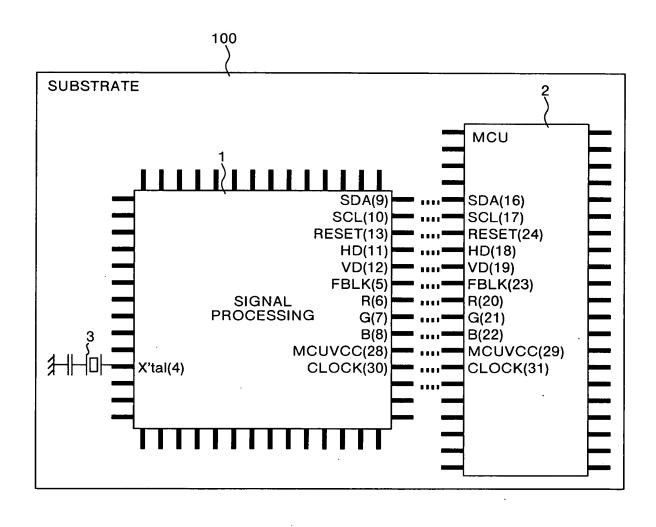
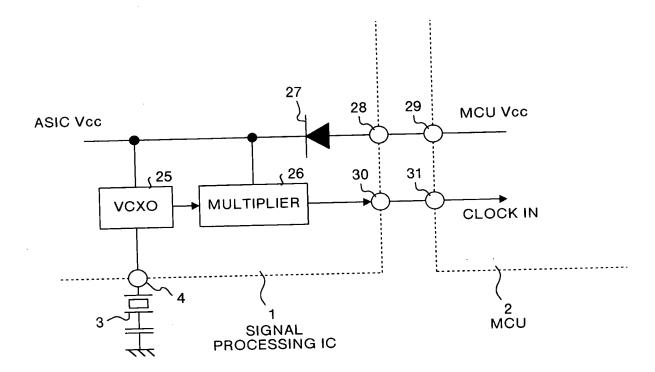


FIG.1



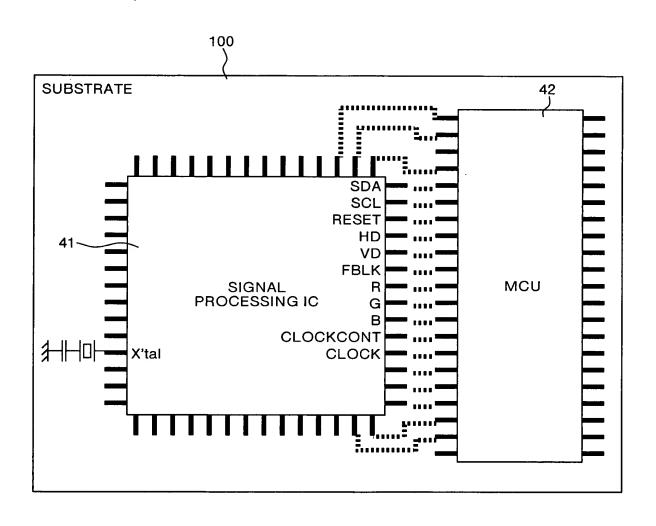
Title: Semiconductor Integrated Device Inventors: Kazuyuki KIKUCHI Atty Docket No.: 401071/SAKAI Leydig, Voit & Mayer, Ltd. 202-737-6770 2/7

FIG.2



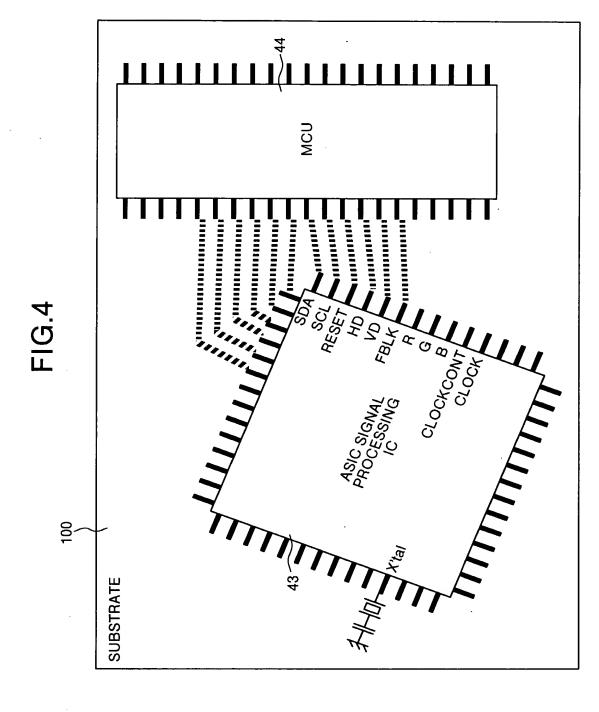
Title: Semiconductor Integrated Device Inventors: Kazuyuki KIKUCHI Atty Docket No.: 401071/SAKAI Leydig, Voit & Mayer, Ltd. 202-737-6770 3/7





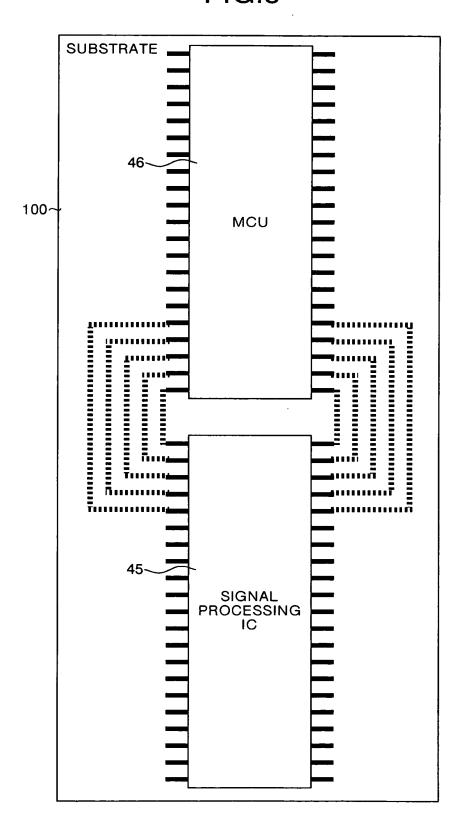
Title: Semiconductor Integrated Device Inventors: Kazuyuki KIKUCHI Atty Docket No.: 401071/SAKAI Leydig, Voit & Mayer, Ltd. 202-737-6770 4/7





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FIG.5

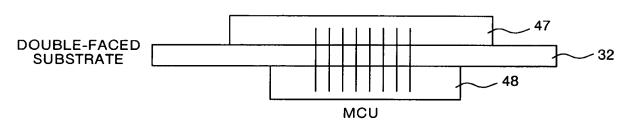




Title: Semiconductor Integrated Device Inventors: Kazuyuki KIKUCHI Atty Docket No.: 401071/SAKAI Leydig, Voit & Mayer, Ltd. 202-737-6770 6/7

FIG.6

SIGNAL PROCESSING IC



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() R(20) () G(21) () B(22) FBLK(23) RESET(24) CLOCK(15) MCU SDA(16) SCL(17) HD(18) VD(19) SDA(9) SCL(10) (8)8 (7)ව (a)a RESET(13) FBLK(5) X'tal(4)